

Layout								
Function		Pinnacle	Ascent LX	Ascent	PCB Planner	PCB Viewer	PCB Browser	
General								
Design rule checking		◆	◆	◆	◆	◆	◆	
Redlining of design						◆	◆	
Read-only view of layout						◆	◆	◆
Schematic Integration								
Data (forward/back-annotation)		◆	◆	◆	◆	◆		
Cross probe		◆	◆	◆	◆	◆	◆	
Constraints Entry								
Constraint editing		◆	◆	◆	◆	◆	Read only	Read only
Electrical constraint editor	Electrical CES	Option	Option	Option	Option	Option	Read only	Read only
Interactive Routing								
Fanout / plow / route selected netlines		◆	◆	◆	◆	◆		
Move / shove part with re-route		◆	◆	◆	◆	◆ (no re-route)		
Multiplow		◆	◆	◆	◆	◆		
Automatic Routing								
Memory / fanout / route		◆	◆	◆				
Via minimization, trace spread & expand		◆	◆	◆				
Advanced Interconnect Design								
Build-up / microvia rule definition		◆	◆	◆	◆	◆		
Build-up / microvia interactive routing		◆	◆	◆	◆	◆		
Build-up / microvia autorouting	Advanced Interconnect	◆	Option	Option				
High-Speed Design								
High speed rules definition		◆	◆	◆	◆	◆		
Interface to HyperLynx and ICX Pro		◆	◆	◆	◆	◆	◆	
Hazard review of HS violations	High Speed Module	◆	◆	HSR Option	HSR Option	◆	◆	
Interactive tuning	High Speed Module	◆	◆	HSR Option	HSR Option	◆		
Interactive route/plow differential pairs	High Speed Module	◆	◆	HSR Option	HSR Option			
Automatic tuning for delay & crosstalk	High Speed Module	◆	◆	HSR Option				
Automatic differential pair routing	High Speed Module	◆	◆	HSR Option				
Test Point Assignment								
Interactive test point placement		◆	◆	◆	◆			
Auto assign test points		◆	◆	◆				
Layout Options								
Design reuse	Exp Design Reuse	Option	Option	Option	Option			
TeamPCB	TeamPCB - WG	Option	Option	Option	Option			
Concurrent team design - host	Xtreme Design Session WG	Option	Option					
Concurrent team design - client	Xtreme Design Client WG	Option	Option					
Variant management	Variant Mgr Physical SW	Option	Option	Option	Option			
Topology planning	Topology Planner	Option	Option	Option	Option			
Topology routing	Topology Router	Option	Option	Option				
Flex circuit design	ATP Flex for Expedition PCB	Option	Option	Option	Option	Option		
RF design	ATP RF Design	◆	Option	Option	Option	Option		
Embedded passive design	ATP Embedded Passives	◆	Option	Option	Option			
Bi-Directional Interfaces								
DXF		◆	◆	◆	◆			
IDF 2.0/3.0		◆	◆	◆	◆	◆		
Basic automation	Exp Basic Automation	◆	◆	◆	◆	◆	Option	
Process automation	Expedition Automation Pro	Option	Option	Option	Option	Option	Option	
Interfaces								
OrCAD schematic I/F	OrCAD Schematic Exp IF	Option	Option	Option	Option			
PADS ASCII reader		◆	◆	◆	◆			
EDIF netlist		◆	◆	◆	◆			
Manufacturing								
ODBG/ODB++ , Gerber, NCDrill output		◆	◆	◆	◆			
Gerber output		◆	◆	◆	◆			
NC drill output		◆	◆	◆	◆			
ATE, AIS, GenCAD output		◆	◆	◆	◆			
Report Writer (Windows only)	Report Writer	◆	◆	◆	◆	◆	◆	
C-LINK (Windows only)	Manufacturing Toolbox	◆	◆	Option	Option			
FabMaster (Windows only)	Manufacturing Toolbox	◆	◆	Option	Option			
FabLink XE	FabLink XE	◆	Option	Option	Option	Option		
FabLink XE Pro	FabLink XE Pro	◆	Option	Option	Option	Option		
Design for fabrication analysis	PCB DFF Analysis	Option	Option	Option	Option			
CAM Output Manager Admin (Windows only)	CAM Output Manager-Admin	N/A	Option	Option	Option	Option		
CAM Output Manager Client (Windows only)	CAM Output Manager-Client	N/A	Option	Option	Option	Option		